

3571 U.S. PTO
10/033590
12/27/01

PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10033590	12/27/2001	257	778	9212	MAI TRAN

**APPLICANTS: Su Jin;

BEST AVAILABLE COPY

**CONTINUING DATA VERIFIED: *none yet*

FOREIGN APPLICATIONS VERIFIED: *none yet*

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO
35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no		42390P13376
Verified and Acknowledged Examiners's initials		
TITLE : Flip-chip opto-electronic circuit		

U.S. DEPT. OF COMM./PAT. & TM-PTO-436L (Rev. 12-99)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
Assistant Examiner		Total Claims	Print Claim for O.G.
		DRAWING	
Amount Due		Sheets Drwg.	Figs. Drwg.
Date Paid		Print Fig.	
<input type="checkbox"/> TERMINAL DISCLAIMER		Application Examiner	
		PREPARED FOR ISSUE	
WARNING: The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368, Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.			

FILED WITH:

☐ DISK (CRF)

☐ CD-ROM

(Attached in pocket on right inside flap)